

### 3rd IEEE ComSoc GraTE-7 (2021) Short Listed Applicants

Sl No	Name	Title of the work	Institution	Guide/Co-guide
1	Dr. Poornima S	Transmit Power Adaptation for Enhancing the Performance of Wireless Multi-hop Half/Full Duplex Underlay Cognitive Relay Networks	National Institute of Technology Calicut, Kerala, India	Dr. A.V. Babu
2	Dr. Upama Rajan M.N	Investigations on MAC Layer Performance of Millimeter Wave Wireless LAN and PAN Standards.	National Institute of Technology Calicut, Kerala	Dr. A.V. Babu
3	Dr Kala S	Accelerating Two Dimensional FFT and Convolutional Neural Networks on FPGA	Division of Electronics, SOE, Cochin University of Science And Technology, Kochi-22, Kerala	Dr. Babita Ros
4	Dr B. Uma Maheswari	Reputation based Delay-resistance and Reliable Hybrid Overlay Structures for Peer-to-Peer Video Streaming.	Amrita School of Engineering, Amrita Vishwa Vidyapeetham, Kasavanahalli, Bangalore - 560035, Karnataka	Dr. T. K. Rame
5	Dr K. RAJESH RAO	Development of Secure Cloud Services for Storing and Accessing Data in Role Based Access Control Applications	Manipal Institute of Technology, Manipal Academy of Higher Education, Manipal, 576104	Dr. Ashalatha I
6	Dr Yoghitha Ramamoorthi	Resource Allocation in Cellular Networks with Base Station Sleeping	Indian Institute of Technology Hyderabad	Dr Abhinav Kumar
7	Dr Bejoy Abraham	Computer-aided Diagnosis and Grading of Prostate Cancer from MRI using Machine Intelligence Techniques	Department of Computer Science, University of Kerala	Dr. Madhu S. Nair, Cochin University of Science and Technology (CUSAT), Kochi;
8	Dr Yashwanth N	Node Localization in Underwater Wireless Sensor Networks	Visvesvaraya Technological University	Dr. B R Sujatha, Malnad College of Engineering, Hassan ;
9	Dr Ashok Bansiwal	Equivalent Circuit Analyses and Methods to Enhance Bandwidth of Klystron Reentrant Cavities	Indian Institute of Science (IISc), Bangalore, Karnataka-560012	Dr. K. J. Vinoy; Dr. Subrata Kumar Datta, DRDO,
10	Dr. Aravinda K.	Architecture and Design of an Ineger-N Charge Pump Phase Locked Loop for 2.4GHz Wireless Applications.	Amrita School of Engineering, Kasavanahalli, Bangalore - 560 035.	Dr. T. K. Rame
11	Dr Sachin Dattatraya Patil	Mitigating the Bufferbloat Problem to Reduce Internet Transport Latency	National Institute of Technology Karnataka, Surathkal, Mangalore, 575025, Karnataka	Dr. Mohit P. T

	<b>M Techs Name</b>	<b>Master Category</b>	<b>Title of The work</b>		
1	Reshma K		Incremental Relaying for Cooperative Non-Orthogonal Multiple Access (NOMA) System : Performance Analysis and Optimization	NIT Calicut	Dr. A. V. Babu,
2	Tirumalasetty Sri Sai Apoorva		Design of Multiband Millimeter Wave Array Antenna for 5G Communications	Amrita Vishwa Vidyapeetham, Bengaluru Campus, Kasavanahalli,	Dr Navin Kumar
3	C.Chandan Kumar		High Throughput Basic set Trellis Min-Max Non-Binary LDPC Code decoder Architecture over GF(4)	Amrita University, Bangalore	Dr.Ramesh Chinthala